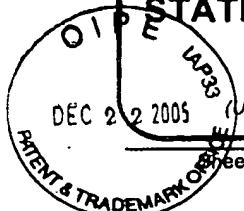


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DISCLOSURE
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Sheet 1 of 2



Application Number	10/666,948
Filing Date	September 19, 2003
First Named Inventor	Stephen J. Smith
Art Unit	2115
Examiner Name	Suresh Suryawanshi
Attorney Docket Number	174/161 Cont.
Confirmation Number	7049

U. S. PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)			
SKS		5,541,849	07-30-1996	Rostoker et al.	
SKS		5,819,064	10-06-1998	Razdan et al.	
SJS		6,044,211	03-28-2000	Jain	
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Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Country Code ³ Number ⁴ Kind Code ⁵ (if known)				
SJS		WO 97/09930	03-20-1997	Medison Co., Ltd.		

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹		
SKS		IBM, "Programmable Manual Cable Assembly Board," IBM Technical Disclosure Bulletin, Vol. 31, No. 12, May 1989, pp. 306-309.	
SJS		Nanya, "Asynchronous VSLI System Design," ASP-DAC '98 Tutorials, February 10, 1998, Yokohama, Japan.	

Examiner Signature *Suresh K Suryawanshi*

Date Considered *3/1/06*

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Sheet 2 of 2

Suresh

Nanya et al. "Scalable-Delay-Insensitive Design: A high-performance approach to dependable asynchronous systems," Proceedings of International Symposium on Future of Intellectual Integrated Electronics, March, 1999, pp. 1-10.

Examiner
Signature

Suresh K. Suryawanshi

Date
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7/1/06

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